

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, ON THE DATE INDICATED BELOW.

BY: Gladys E. Morales Date: April 19, 2006



MAIL STOP AMENDMENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor:	Chyh-Yih Chang	§	
		§	
		§	
Conf. No.:	9385	§	Group Art Unit: 2822
		§	
		§	Examiner: Kiesha L. Rose
Application No.:	10/702,437	§	
		§	
Filing Date:	November 7, 2003	§	Attorney Docket No.: 681954-166U2
		§	
		§	
Title:	SUBSTRATE-BIASED SILICON DIODE FOR ELECTROSTATIC DISCHARGE PROTECTION		

AMENDMENT

This paper is being submitted in response to the Office Action mailed January 26, 2006 and is being timely filed within the shortened statutory period set for a response to the Office Action. No fee is believed to be due for filing this response. However, if any fee is due, charge the fee to Deposit Account No. 50-1017 (Billing No. 681954.0170).

Please amend the application as follows:

Amendments to the Drawings are on page 2 of this paper.

Listing of the Claims are on page 3 of this paper.

Remarks and Conclusion begin on page 6 of this paper.